

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	((("20040019703") or ("20020019926")).PN.	USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 10:46
S2	10074	709/230,236,237,246,250.ccls. or 710/62-66,72,105.ccls. or 370/463-476.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 11:28
S3	314	(709/230,236,237,246,250.ccls. or 710/62-66,72,105.ccls. or 370/463-476.ccls.) and (processor CPU) and ((memory near3 controller) (north bear3 bridge)) and (adapter with memory)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 11:36
S4	19	(709/230,236,237,246,250.ccls. or 710/62-66,72,105.ccls. or 370/463-476.ccls.) and (processor CPU) and ((memory near3 controller) (north bear3 bridge)) and (adapter with memory with slot)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 11:36
S5	195	(709/230,236,237,246,250.ccls. or 710/62-66,72,105.ccls. or 370/463-476.ccls.) and (processor CPU) and ((memory near3 controller) (north bear3 bridge)) and (adapter with memory with (slot bus))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 11:40
S6	147	(709/230,236,237,246,250.ccls. or 710/62-66,72,105.ccls. or 370/463-476.ccls.) and (processor CPU) and ((memory near3 controller) (north near3 bridge)) and (adapter with memory with (slot bus))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 14:46
S7	391	interleav\$5 with memory near3 controller	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 16:05
S8	151	interleav\$5 near3 memory near3 controller	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 16:05
S9	48	(processor CPU) with ((I/O PCI ISA EISA AGP SM memory) near3 controller) with (memory near3 slot)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 17:07
S10	0	(processor CPU) with ((north near3 bridge) northbridge) with (memory near3 slot)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 17:06
S11	1	(processor CPU) with ((north near3 bridge) northbridge) with (memory near3 (slot bank))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 17:06
S12	260	(processor CPU) with ((I/O PCI ISA EISA AGP SM memory) near3 controller) with (memory near3 (bank slot))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 17:26
S13	7	(processor CPU) with ((I/O PCI ISA EISA AGP SM) near3 memory near3 controller) with (memory near3 (bank slot))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 17:24
S14	0	"6047348.did"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 17:24
S15	1	"6047348".did.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/12 17:24

S16	3	"6247110".pn. or "6076152".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/13 11:22
S17	136	((adapter expansion) near3 (board card port controller)) with (memory near3 slot)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/13 12:06
S18	185	((adapter expansion) near3 (board card port controller)) with (memory near3 slot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/13 12:12
S19	211	((adapter expansion) near3 (board card port controller)) with (memory near3 (slot bank))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/13 14:09
S20	1	"6052134".pn. and interleav\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/13 14:09
S21	23	((adapter) or (expansion near3 (board card port controller))) same ((address command) near3 decoder) same ((read\$3 writ\$3) near3 (buffer register))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 13:27
S22	13	((((adapter) or (expansion near3 (board card port controller))) same ((address command) near3 decoder) same ((read\$3 writ\$3) near3 (buffer register))) and (configuration status) near3 register	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 11:07
S23	1	"5295246".pn. and (status near3 register) same (control near3 register)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 11:23
S24	61	((I/O PCI ISA EISA AGP SM) near3 (controller)) with (transfer\$4 transmit\$4 inform\$7) with (adpater expansion)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 11:25
S25	362	RIMM	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 12:17
S26	56	RIMM with DIMM	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 12:17
S27	3	((adapter) or (expansion near3 (board card port controller))) same (FPGA PLA PLD reconfigur\$5) same (dual-port\$3 near3 memory)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 13:29
S28	276	((adapter) or (expansion near3 (board card port controller))) same (FPGA PLA PLD reconfigur\$5) same (memory)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 13:30
S29	57	((adapter) or (expansion near3 (board card port controller))) with (FPGA PLA PLD reconfigur\$5) with (memory)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 13:38
S30	685	chain near3 port	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 13:38

S31	1	(chain near3 port) with (adapter)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 13:39
S32	8	US-5903771-\$.DID. OR US-6192439-\$.DID. OR US-6076152-\$. DID. OR US-6052773-\$.DID.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/07/14 16:39
S33	200	(cluster near5 fabric)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 10:06
S34	18	(cluster near3 interconnect near3 fabric)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 09:48
S35	0	(cluster near5 fabric) with (PSTN cellular)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 10:06
S36	780	cluster with (PSTN cellular)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 10:07
S37	0	cluster with fabric with PSTN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 10:07
S38	0	cluster with interconnect with PSTN	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 10:08
S39	24	cluster with PSTN	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 10:08.
S40	31	RIMM.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/04 11:21
S41	19	"4575780"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 09:15
S42	12	"4575780".did. "6289398".did. "6058451".did. "5214768".did. "6295571".did. "5903911".did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/05 09:26
S43	59	(processor CPU) with ((I/O PCI ISA EISA AGP SM memory) near3 controller) with (memory near3 slot)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 16:22

S44	227	((adapter expansion) near3 (board card port controller)) with (memory near3 slot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 16:38
S45	2	"6295571".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/13 16:43
S46	12	((adapter expansion) with (port card board controller)) with direct with (bus connection) with (memory near5 (module slot))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 13:08
S47	279	((adapter expansion) with (port card board controller)) with (bus connection) with (memory near5 (module slot))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 13:11
S48	243	((adapter expansion PCI ISA EISA SM) with (port card board controller)) with (control command) with (memory near5 (module slot))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/14 13:12
S49	2	"6633945".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 09:34
S50	298	((adapter expansion) with (port card board controller)) with (bus connection) with (memory near5 (module slot))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 08:52
S51	8668	(slot module) with controller with (direct coupl\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 08:55
S52	81	(memory near3 (slot module)) with controller with (direct\$4 near3 (coupl\$4 connect\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/27 08:56
S53	0	((processor) with (controller (north near3 bridge)) with (memory near5 slot) with (adapter) with (direct near3 (connect\$4 communicat\$5))).clm.	US-PGPUB	OR	ON	2005/10/27 12:43
S54	0	((processor) with (memory near5 slot) with (adapter) with (direct near3 (connect\$4 communicat\$5))).clm.	US-PGPUB	OR	ON	2005/10/27 12:43
S55	0	((memory near5 slot) with (adapter) with (direct near3 (connect\$4 communicat\$5))).clm.	US-PGPUB	OR	ON	2005/10/27 12:44